



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/065,210	09/25/2002	Raj Kumar Jain	2000P19158US	8170
7590	10/20/2003		EXAMINER	HO, HOAI V
Siemens Intellectual Property Technology Consulting Pte Ltd. 166 Kallang Way 6th Floor Singapore, 349249 SINGAPORE			ART UNIT	PAPER NUMBER
2818				
DATE MAILED: 10/20/2003				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/065,210	JAIN, RAJ KUMAR	
	Examiner Hoai V. Ho	Art Unit 2818	

-- The MAILING DATE of this communication appears on the cover sheet with the corresponding address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-20 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 25 September 2002 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some *
 - c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s) _____.
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.	6) <input type="checkbox"/> Other: _____.

1. Claims 1-20 are presented for examination.

Drawings

2. It is requested that all blocks of Figure 1 be labeled with English language descriptions.

For example, a block 11 will be labeled as "Row Decoder." Correction is required.

Specification

Abstract

3. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

4. The abstract of the disclosure is objected to because of the legal phraseology "comprises".

Correction is required. See MPEP § 608.01(b).

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 1-20 are rejected under 35 U.S.C. 102(ab) as being anticipated by Hatakeyama U.S. Patent No. 5617364.

As per claims 1 and 6-18, Figures 3, 6, 7, 8 and 15 of Hatakeyama are directed to an integrated circuit (IC) comprising: an array of memory cells arranged in rows and columns (101), an address bus for receiving addressing information (an input to 105), a data-in path for receiving data during a write memory access (an input to 121), a signal out path coupled to the memory array for outputting data during a read (an input to 122); a data-out path (an input to 122) coupled to the signal out path; and a redundancy unit (130 or 140) comprising, at least one redundancy element having tag, address, and data portions, the tag portion indicates whether the redundancy element has been programmed for redundancy, the address portion contains a defective address if the redundancy element has been programmed for redundancy, and the data portion contains data associated with the defective address (fig. 6 or 7), a control circuit (58a and 58b or 64a or 64b) for generating an active signal indicating that an access is associated with the defective address, a read redundancy control circuit (119 or fig. 8, col. 9, lines 23-38) coupled to the output signal path and redundancy unit, and a write redundancy control unit (119 or fig. 8) coupled to the data in path and redundancy unit. See col. 7, line 7 to col. 9, line 62, col. 15, lines 32-43 and col. 16, line 3-5.

As per claims 2-5, Hatakeyama, starting at column 16, lines 3-5, discloses wherein the memory cells comprise SRAM, DRAM, multi-port DRAM, or multi-port DRAM with SRAM functionality cells.

As per claims 19 and 20, Hatakeyama, starting at column 1, lines 55-65 and column 15, line 45-64, discloses further comprising a test control unit performing a test to determine defective addresses.

7. The prior art made of record and not relied upon is considered pertinent to applicants' disclosure. Fujita (5379259) discloses a semiconductor memory with redundancy circuit.

8. When responding to the office action, Applicants are advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist the examiner to locate the appropriate paragraphs.

9. A shortened statutory period for response to this action is set to expire 3 (three) months and 0 (zero) day from the date of this letter. Failure to respond within the period for response will cause the application to become abandoned (see MPEP 710.02 (b)).

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to whose telephone number is (703) 308-4839. Other inquiries of this application should be called to (703) 308-0956 or the fax number (703) 872-9306.



H. Ho
October 3, 2003



Hoai V. Ho
Primary Examiner
Art Unit 2818